R40 ~ R42 and R34 ~ R36 are the damping resistors for GMII mode. These resistor values can be adjusted depending on the signal integrity and EMI status.

Close to MAC

Close to IP1001

Place 0 ohm for measuring Giga template when MAC driver is not ready to write register 9 of IP1001.

2.1V regulator output by IP1001

1.8v is better for EMI solution.

1.2V regulator output by IP1001

Do not leave this pin unconnected. Connect this pin to GND through a resistor if the MAC side does not provide TX-ER.

Now PHY address setting 00001b

PHY address setting[0:4]

125MHz clock output

GMII Interface

Select either pull-high resistor or pull-low resistor for Tx/Rx phase delay. Reserve these resistors on PCB for phase adjustment.

Power supply from system

As close as possible to the IC.

The lower supply voltage is better for EMI solution.

Transformer :
FUN-JIN->GT24-03S,GT24-03SM
Pulse->H5004, H5007, H5062, H5091NL
Bothhand->24HST1041A-2,GST5009

RJ45

Notice:
1) CHANNEL A+ is mapping to pin 1 of RJ45.
2) CHANNEL D- is mapping to pin 8 of RJ45.

Notice: IP1001 LED pins can be selected either 1G or 100M/10M mode for MAC chip operation.

LED Mode 0

10/100M Link/Act

100M Link/Act

1G Link/Act

IC Plus Semiconductor Corporation

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