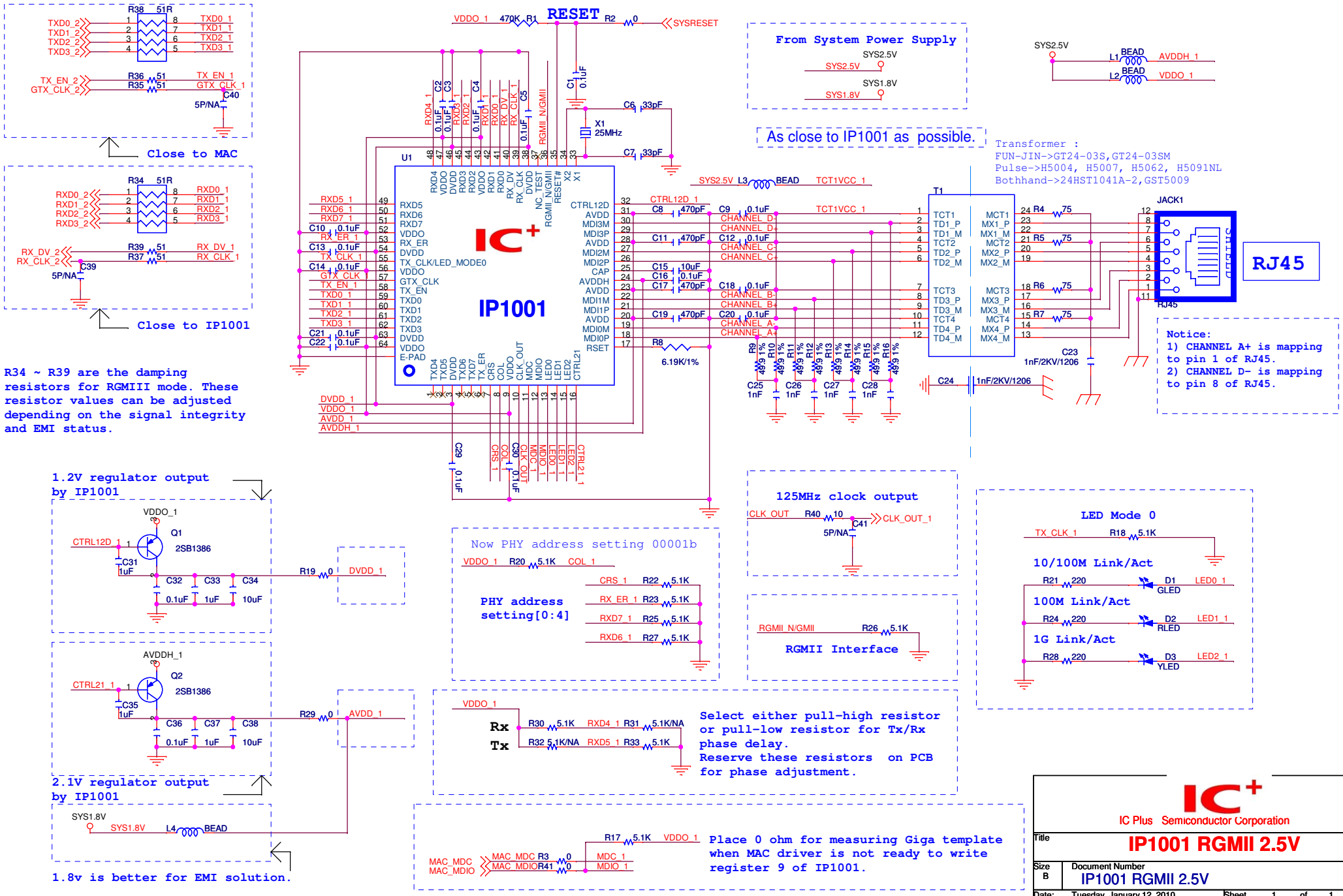
R34 ~ R39 are the damping resistors for RGMIII mode. These resistor values can be adjusted depending on the signal integrity and EMI status.
Close to MAC
Close to IP1001
RESET
IP1001
From System Power Supply
As close to IP1001 as possible.
Transformer :
FUN-JIN->GT24-03S,GT24-03SM
Pulse->H5004, H5007, H5062, H5091NL
Bothhand->24HST1041A-2,GST5009
RJ45
Notice:
1) CHANNEL A+ is mapping to pin 1 of RJ45.
2) CHANNEL D- is mapping to pin 8 of RJ45.
1.2V regulator output by IP1001
2.1V regulator output by IP1001
1.8v is better for EMI solution.
Now PHY address setting 00001b
PHY address setting[0:4]
125MHz clock output
RGMII Interface
LED Mode 0
10/100M Link/Act
100M Link/Act
1G Link/Act
Select either pull-high resistor or pull-low resistor for Tx/Rx phase delay.
Reserve these resistors on PCB for phase adjustment.
Place 0 ohm for measuring Giga template when MAC driver is not ready to write register 9 of IP1001.
IC Plus Semiconductor Corporation
IP1001 RGMII 2.5V
Size B
Document Number IP1001 RGMII 2.5V
Rev A.5
Date: Tuesday, January 12, 2010
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